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approaches to computing hardware and RF power amplifiers, and explains how emerging trends and system demands of computing and telecommunications systems influence the choice, design and operation of semiconductors. Next, the field effect devices are described, including

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telecommunications systems. This is  
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offs between various practically  
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of basic electronic devices, including the diode, bipolar junction transistor, field effect transistor, operational amplifier and current feedback amplifier. They then present comprehensive instruction on the design of working, realistic electronic circuits of varying levels of complexity,

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including power amplifiers, regulated  
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functioning circuits, throughout every chapter, with an emphasis on real applications; Includes numerous exercises at the end of each chapter; Uses simulations to demonstrate the functionality of the designed circuits; Enables readers to design important electronic circuits including amplifiers,



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and power ICs. One of the unique  
features of the Power Electronics  
Design Handbook is the integration of  
component and system theory with  
practical applications, particularly  
energy-saving low-power applications.

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Many chapters also include a section that looks forward to future developments in that area. References for further information or more in-depth technical reading are also included.

Nihal Kularatna is a principal research engineer with the Arthur C. Clarke Foundation in Sri Lanka. He is also the

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author of Modern Electronic Test and  
Measuring Instruments, published by  
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During the first decade following the invention of the transistor, progress in semiconductor device technology advanced rapidly due to an effective synergy of technological discoveries and physical understanding. Through physical reasoning, a feeling for the

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right assumption and the correct  
interpretation of experimental findings,  
a small group of pioneers conceived  
the major analytic design equations,  
which are currently to be found in  
numerous textbooks. Naturally with the  
growth of specific applications, the  
description of some characteristic

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properties became more complicated. For instance, in integrated circuits this was due in part to the use of a wider bias range, the addition of inherent parasitic elements and the occurrence of multi dimensional effects in smaller devices. Since powerful computing aids became available at the same

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time, complicated situations in A To  
complex configurations could be  
analyzed by useful numerical  
techniques. Despite the resulting  
progress in device optimization, the  
above approach fails to provide a  
required compact set of device design  
and process control rules and a



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compact circuit model for the analysis  
of large-scale electronic designs. This  
book therefore takes up the original  
thread to some extent. Taking into  
account new physical effects and  
introducing useful but correct  
simplifying assumptions, the previous  
concepts of analytic device models

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have been extended to describe the characteristics of modern integrated circuit devices. This has been made possible by making extensive use of exact numerical results to gain insight into complicated situations of transistor operation.

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Proceedings of the International  
Conference held at Seville, Spain,  
October 27-31, 1986.

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This book describes the bottleneck To faced soon by designers of traditional CMOS devices, due to device scaling, power and energy consumption, and variability limitations. This book aims at bridging the gap between device technology and architecture/system design. Readers will learn about

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challenges and opportunities  
presented by [beyond-CMOS devices]  
and gain insight into how these might  
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