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Abstract: The key
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Different logic families are used in the design for various logic modules.

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Abstract: The design and characteristics of a Nb based Josephson 16-bit arithmetic logic unit (ALU) for use as a major component of a practical Josephson

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microprocessor
are discussed.
The ALU has 900
gates and uses
dual-rail logic
to perform 12
functions. One
of the simplest
algorithms, the
ripple-carry
method, is used.
A subnanosecond
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An optimum VLSI
design of a
16-BIT ALU
Abstract—In this
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design for

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Arithmetic and Logical Unit is proposed. This design of 16 bit ALU consists of two input pins for 16-bit operands along with one input pin for 5 selection lines at the input and at the output a pin for the

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Abstract: The
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Arithmetic Logic
Unit (ALU) is
one of the most
important
components of

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any system and is used in many appliances like calculators, cell phones, and computers. A 32-bit ALU was designed using Verilog HDL with the logical gates such as AND and OR for each one bit ALU circuit. The

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implemented in
Xilinx. It can
work fast than
the ALU
processor using
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The contents
herein are
intended to
provide an
update to an
earlier volume
on the same

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Subject (based
on a NATO ASI
held in 1988).
Four years seems
a relatively
short time
interval, and
our title
itself,
featuring The
New
Superconducting
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appear somewhat

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Nevertheless, we feel strongly that the ASI fostered a timely reexamination of the technical progress and application potential of this rapid-paced field. There are, indeed,

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Using Whall
many new avenues
for
technological
innovation which
were not
envisioned or
considered
possible four
years ago. The
greatest
advances by far
have occurred
with regard to
oxide

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Using VLSI
superconductors,
the so-called
high transition-
temperature
superconductors,
known in short
as HTS. These
advances are
mainly in the
ability to
fabricate both
(1) high-
quality,
relatively large-

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filters and (2)
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(SNS) Josephson
junctions, for s
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magnetometers.
Additionally, we
have seen the
invention and
development of
the flux-flow
transistor, a
planar three-
terminal device.
During the
earlier ASI only
the very first

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HTS films with adequate critical-current density had just been fabricated, and these were of limited area and had high resistance for microwave current.

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omagnetics, Tele
communication
Engineering and
Low Power VLSI
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This book
provides a
structured
treatment of the
key principles

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and techniques
for enabling
efficient
processing of
deep neural
networks (DNNs).
DNNs are
currently widely
used for many
artificial
intelligence
(AI)
applications,
including

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Using Whu
computer vision,
speech
recognition, and
robotics. While
DNNs deliver
state-of-the-art
accuracy on many
AI tasks, it
comes at the
cost of high
computational
complexity.
Therefore,
techniques that

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enable efficient processing of deep neural networks to improve key metrics—such as energy-efficiency, throughput, and latency—without sacrificing accuracy or increasing hardware costs

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Using critical to enabling the wide deployment of DNNs in AI systems. The book includes background on DNN processing; a description and taxonomy of hardware architectural approaches for designing DNN

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