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UPSOL ACADEMY Q. 3.31: Write a Verilog gate-level description of the circuit shown in(a) Fig. 3.22 (a)

Q. 2.14: Implement the Boolean function $F=xy + x'y' + y'z$ (a) With AND, OR, and inverter gates

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Digital Design: Q. 1.9:

Express the following

numbers in decimal: (a)

$(10110.0101)_2$ (b) $(16.5)_{16}$

Q. 4.11: Using four half-adders (HDL—see Problem

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4.54), (a) Design a full-
subtractor circuit incremen
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1.6. $(x - 3)(x - 6) = x^2 -$
 $(6 + 3)x + 6*3 = x^2 - 11x +$
22 Therefore: $6 + 3 = b +$
 $1m$, so $b = 8$ Also, $6*3 =$
 $(18)10 = (22)8$. 1.7. 64CD16

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