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Computer Engineering: A DEC View of Hardware Systems Design focuses on the principles, progress, and concepts in the design of hardware systems. The selection first elaborates on the seven views of computer systems, technology progress in logic and memories, and packaging and manufacturing. Concerns cover power supplies, DEC computer packaging generations, general packaging, semiconductor logic technology, memory technology, measuring (and creating) technology progress, structural levels of a computer system, and packaging levels-of -integration. The manuscript then examines transistor circuitry in the Lincoln TX-2, digital modules, PDP-1 and other 18-bit computers, PDP-8 and other 12-bit computers, and structural levels of the PDP-8. The text takes a look at cache memories for PDP-11 family computers, buses, DEC LSI-11, and design decisions for the PDP-11/60 mid-range minicomputer. Topics include reliability and maintainability, price/performance balance, advances in memory technology, synchronization of data transfers, error control strategies, PDP-11/45, PDP-11/20, and cache organization. The selection is a fine reference for practicing computer designers, users, programmers, designers of peripherals and memories, and students of computer engineering and computer science.

An introduction to computer engineering for babies. Learn basic logic gates with hands on examples of buttons and an output LED.

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This book provides comprehensive insights into the field of computer engineering and information technology. Some of the diverse topics covered in this book are data processing, data analysis techniques, software engineering, multimedia, etc. Those with an interest in the field of computer engineering and information technology would find this book helpful as it contains contributions by internationally renowned scientists and experts that bring forth new frontiers for further research.

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This text introduces the discipline of computer engineering to engineering students. It discusses the principle issues of data representation and develops the basic logic circuits for data manipulation. It closely examines a conventional though simple computer, along with an assembler language suitable to its architecture and close to the IEEE-694 standard. The interplay of hardware design and software structure is stressed throughout, and is illustrated by examples ranging from string manipulation to input-output management. The text is distinguished by its clear, straightforward writing style, and is accompanied by an MS-DOS disk containing a logic circuit simulator, an assembler, and a computer simulator. The disk includes copies of all examples in the book, allowing further exploration of logic circuits and step-by-step examination of central processor operation.

This book includes the proceedings of the second International Conference on Advances in Computer Science and Engineering (CES 2012), which was held during January 13-14, 2012 in Sanya, China. The papers in these proceedings of CES 2012 focus on the researchers' advanced works in their fields of Computer Science and Engineering mainly organized in four topics, (1) Software Engineering, (2) Intelligent Computing, (3) Computer Networks, and (4) Artificial Intelligence Software.

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